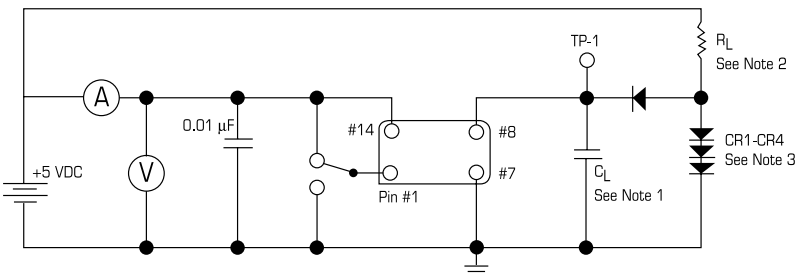




## TRI-STATE TTL CLOCK OSCILLATORS (FULL & HALF SIZE)

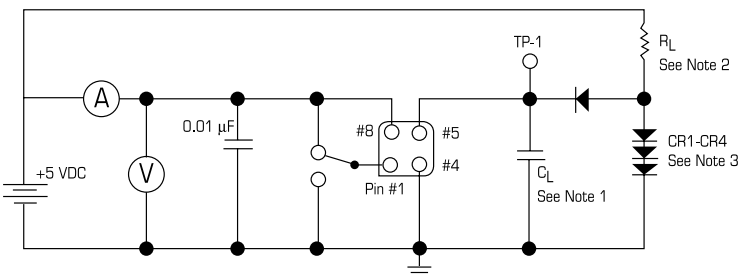
**MODEL CU**  
**5.0VDC**

Model	CU	CU
Frequency Range	10kHz ~ 69.999MHz	70MHz ~ 160.0MHz
Frequency Stability	100ppm Standard, Optional Tolerances Available	100ppm Standard, Optional Tolerances Available
Operating Temperature Range	0°C ~+70°C, Extended Temperature Ranges Available	0°C ~+70°C, Extended Temperature Ranges Available
Storage Temperature Range	-55°C ~+125°C	-55°C ~+125°C
Current Consumption	10.0KHz ~ 23.999MHz: 15mA Max. 24.000MHz ~ 69.999MHz: 30mA Max.	70.0MHz ~ 99.9MHz: 30mA Max. 100.00MHz ~ 129.9MHz: 35mA Max. 130.00MHz ~ 160.00MHz: 40mA Max.
Supply Voltage	+5 VDC ± 10%	+5 VDC ± 10%
Symmetry	60/40 @ 50% Vcc, Optional Tolerances Available	60/40 @ 50% Vcc, Optional Tolerances Available
Rise & Fall Time (Tr & Tf)	10 nSec Max	4 nSec Max
Logic "1"	2.8 VDC Min	2.8 VDC Min
Logic "0"	0.4 VDC Max	0.4 VDC Max
Output Load	10TTL	2TTL Other Loads Available
Aging	< 5ppm per year	< 5ppm per year
Enable Input	Enable - Logic "1" 2.0 VDC Min Disable - Logic "0" 0.5 VDC Max.	Enable - Logic "1" 2.0 VDC Min Disable - Logic "0" 0.5 VDC Max.



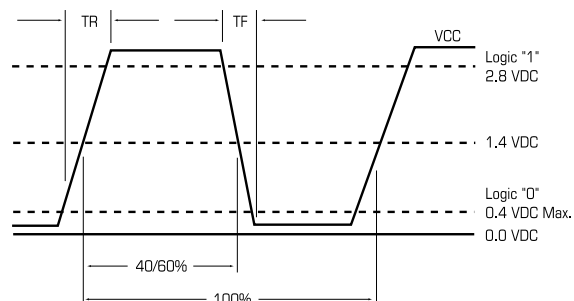
**TEST CIRCUIT FULL SIZE**

FULL SIZE TRI-STATE TTL	
PINS	CONNECTIONS
1	ENABLE / DISABLE
7	GND
8	OUTPUT
14	+5 VDC ±10%



**TEST CIRCUIT HALF SIZE**

HALF SIZE TRI-STATE TTL	
PINS	CONNECTIONS
1	ENABLE / DISABLE
4	GND
5	OUTPUT
8	+5 VDC ±10%



**NOTE:**

1.  $C_L$  Capacitance includes probe and test jig (15pF typical)
2.  $R_L = 400\Omega - 10$  TTL  
 $2K\Omega - 10$  LSTTL
3. All diodes are 1N941, 1N43064 or equivalent